OMRON

CP series CP1H CPU Unit CP1H-XODD-O/CP1H-YODD-O CP1H-XAODO-O

4 Axis Position Control and Comprehensive Programmable Controller

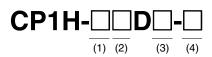
- The CP1H-X with pulse outputs for 4 axes.
- The CP1H-Y with 1-MHz pulse I/O.
- The CP1H-XA with pulse outputs for 4 axes and built-in analog I/O.



Features

- Pulse output for 4 axes. Advanced power for high-precision positioning control.
- High-speed counters. Differential phases for 4 axes. Easily handles multi-axis control with a single unit.
- Eight interrupt inputs are built in. Faster processing of approximately 500 instructions speeds up the entire system.
- Serial communications. Two ports. Select Option Boards for either RS-232C or RS-485 communications.
- Ethernet Communications. Enabled by using an Option Board. Two ports can be used as an Ethernet port to perform. Ethernet communications between the CP1H and a host computer.
- Built-in Analog I/O. XA CPU Units provide 4 input words and 2 output words.
- USB Peripheral Port. Another standard feature.
- The structured text (ST) language. Makes math operations even easier.
- Can be used for the CP1W series and CJ series Unit. The extendibility of it is preeminently good.
- LCD displays and settings. Enabled using Option Board.

Model Number Legend (Not all models that can be represented with the model number legend can necessarily be produced.)



- 1. Class
 - X: Basic model
 - XA : Built-in analog I/O terminals
- 3. Output classification
 - R: Relay output

4. Power supply

A: AC

D: DC

- Т: Transistor Output (sinking) T1 : Transistor Output (sourcing)
- Y: Dedicated pulse I/O terminals
- 2. Number of Built-In number I/O points 40 : 40 I/O points 20: 20 I/O points

International Standards

- The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives.
- Contact your OMRON representative for further details and applicable conditions for these standards.

CPU Units

		Specificati	ons								
CPU Unit	CPU type	Power supply	Output method	Inputs	Outputs	Model	Standards				
CP1H-X CPU Units	Memory capacity: 20K steps High-speed counters:	AC power supply	Relay output			CP1H-X40DR-A					
	100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes	DC power	Transistor output (sinking)	24	16	CP1H-X40DT-D					
	(Models with transistor outputs only)	supply	Transistor output (sourcing)			CP1H-X40DT1-D					
CP1H-XA CPU Units	Memory capacity: 20K steps High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes (Models with transistor outputs only) Analog inputs: 4 Analog outputs: 2	AC power supply	Relay output			CP1H-XA40DR-A					
		Pulse outputs: 100 kHz, 4 axes	Pulse outputs: 100 kHz, 4 axes	Pulse outputs: 100 kHz, 4 axes	Pulse outputs: 100 kHz, 4 axes	DC power	Transistor output (sinking)	24	16	CP1H-XA40DT-D	UC1, N, L, CE
		supply	Transistor output (sourcing)			CP1H-XA40DT1-D					
CP1H-Y CPU Units	Memory capacity: 20K steps High-speed counters: 1 MHz, 2 axes 100 kHz, 2 axes Pulse outputs:1 MHz, 2 axes 100 kHz, 2 axes	DC power supply	Transistor output (sinking)	12 + line-driver input, 2 axes	8 + line-driver output, 2 axes	CP1H-Y20DT-D					

Note: 1. CP1H PLCs are supported by CX-Programmer version 6.1 or higher.

2. Purchase a separately sold Option Unit if you will use RS-232C, RS-422A/485, Ethernet, or LCD.

■ Options for CPU Units

Name	Specifications	Model	Standards
RS-232C Option Board		CP1W-CIF01	UC1, N,
RS-422A/485 Option Board	Can be mounted in either CPU Unit Option Board slot 1 or 2.	CP1W-CIF11	L, CE
RS-422A/485 (Isolated-type) Option Board		CP1W-CIF12	UC1, N, L, CE
Ethernet Option Board	Can be mounted in either CPU Unit Option Board slot 1 or 2. *	CP1W-CIF41	UC1, N, L, CE
LCD Option Board	Can be mounted only in the CPU Unit Option Board slot 1.	CP1W-DAM01	UC1, L, N, CE
Memory Cassette	Can be used for backing up programs or auto-booting.	CP1W-ME05M	UC1, N, L, CE

* When using CP1W-CIF41 Ver.1.0, one Ethernet port can be added.

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Programming Devices

	Specifications				
Name		Number of licenses	Media	Model	Standards
FA Integrated Tool Package CX-One Lite Version 4.⊡	 CX-One Lite is a subset of the complete CX-One package that provides only the Support Software required for micro PLC applications. CX-One Lite runs on the following OS. OS: Windows XP (Service Pack 3 or higher, 32-bit version) / Windows Vista (32-bit/64-bit version) / Windows 7 (32-bit/64-bit version) / Windows 8.1 (32-bit/64-bit version) / Windows 8.1 (32-bit/64-bit version) CX-One Lite Ver. 4. includes Micro PLC Edition CX- 	1 license	CD	CXONE-LT01C-V4	
	Programmer Ver. 9.				
FA Integrated Tool Package CX-One Ver. 4.□	 CX-One is a package that integrates the Support Software for OMRON PLCs and components. CX-One runs on the following OS. OS: Windows XP (Service Pack 3 or higher, 32-bit version) / Windows Vista (32-bit/64-bit version) / Windows 7 (32- bit/64-bit version) / Windows 8 (32-bit/64-bit version) / Windows 8.1 (32-bit/64-bit version) 	1 license (See note 3.)	DVD	CXONE-AL01D-V4	
	CX-One Ver. 4.□ includes CX-Programmer Ver. 9.□.				
Programming Device	Connects Personal Computers, D-Sub 9-pin (Length: 2.0 m)	For anti-static	connectors	XW2Z-200S-CV	
Connecting Cable for CP1W-CIF01 RS-232C	Connects Personal Computers, D-Sub 9-pin (Length: 5.0 m)	T OF ATTE-Static	CONNECTORS	XW2Z-500S-CV	
Option Board	Connects Personal Computers, D-Sub 9-pin (Length: 2.0 m)			XW2Z-200S-V	
(See note 4.)	Connects Personal Computers, D-Sub 9-pin (Length: 5.0 m)			XW2Z-500S-V	
USB-Serial Conver- sion Cable (See note 4.)	USB-RS-232C Conversion Cable (Length: 0.5 m) and PC drive included. Complies with USB Specification 2.0 On personal computer side: USB (A plug connector, male) On PLC side: RS-232C (D-sub 9-pin, male) Driver: Supported by Windows 98, Me, 2000, XP(32bit), Vista(and 8(32bit/64bit)	CS1W-CIF31	N		

Note: 1. CP1H PLCs are supported by CX-Programmer version 6.1 or higher.

Update The CX-Programmer version automatically from the website using CX-Programmer version 7.0 (included with CX-One version 2.0).

2. The CX-One and CX-One Lite cannot be simultaneously installed on the same computer.

3. Multi licenses are available for the CX-One (3, 10, 30 or 50 licenses).

Cannot be used with a peripheral USB port.
 To connect to a Personal Computers via a peripheral USB port, use commercially-available USB cable (A or B type, male).

The following tables lists the Support Software that can be installed from CX-One

Support Software in CX-One		CX-One Lite Ver.4.	CX-One Ver.4.⊡	Support Software in CX-One		CX-One Lite Ver.4.	CX-One Ver.4.⊡
Micro PLC Edition CX-Programmer	Ver.9.	Yes	No	CX-Drive	Ver.1.	Yes	Yes
CX-Programmer	Ver.9.	No	Yes	CX-Process Tool	Ver.5.	No	Yes
CX-Integrator	Ver.2.	Yes	Yes	Faceplate Auto-Builder for NS	Ver.3.	No	Yes
Switch Box Utility	Ver.1.	Yes	Yes	CX-Designer	Ver.3.	Yes	Yes
CX-Protocol	Ver.1.	No	Yes	NV-Designer	Ver.1.	Yes	Yes
CX-Simulator	Ver.1.	Yes	Yes	CX-Thermo	Ver.4.	Yes	Yes
CX-Position	Ver.2.	No	Yes	CX-ConfiguratorFDT	Ver.1.	Yes	Yes
CX-Motion-NCF	Ver.1.	No	Yes	CX-FLnet	Ver.1.	No	Yes
CX-Motion-MCH	Ver.2.	No	Yes	Network Configurator	Ver.3.	Yes	Yes
CX-Motion	Ver.2.	No	Yes	CX-Server	Ver.4.	Yes	Yes

Note: For details, refer to the CX-One Catalog (Cat. No: R134).

CP1H

Expansion Units

Product name	Inputs	Outputs	Output type		Model	Standards	
nput Unit	8		24 VDC Input		CP1W-8ED		
Output Units			Relay	CP1W-8ER			
		8	Transistor (sinking)		CP1W-8ET	U, C, N, L, CE	
			Transistor (sourcing)		CP1W-8ET1		
ā			Relay		CP1W-16ER		
(mmme)		16	Transistor (sinking)		CP1W-16ET	N, L, CE	
E VIERNER E		-	Transistor (sourcing)		CP1W-16ET1		
			Relay		CP1W-32ER		
		32	Transistor (sinking)		CP1W-32ET	N, L, CE	
			Transistor (sourcing)		CP1W-32ET1		
O Units			Relay		CP1W-20EDR1		
(Internet)	12	8	Transistor (sinking)		CP1W-20EDT	U, C, N, L, CE	
			Transistor (sourcing)		CP1W-20EDT1		
<u>, 6</u> 0			Relay		CP1W-40EDR		
Railiterenerite	24	16	Transistor (sinking)		CP1W-40EDT	N, L, CE	
- Plantener			Transistor (sourcing)		CP1W-40EDT1		
Analog Input Unit			Input range: 0 to 5 V, 1 to 5 V, 0 to 10 V, ±10 V, 0 to 20	Resolution: 1/6000	CP1W-AD041	UC1, N, L, CE	
	4CH		mA or 4 to 20 mA	Resolution: 1/12000	CP1W-AD042	UC1, N, CE	
Analog Output Unit		2CH	Output range: 1 to 5 V, 0 to 10 V, ±10 V, 0 to 20 mA, or 4 to 20 mA. F 1	Resolution: 1/6000	CP1W-DA021	UC1, N, L, CE	
		4CH		Resolution: 1/6000	CP1W-DA041	001, N, L, OL	
				Resolution: 1/12000	CP1W-DA042	UC1, N, CE	
Analog I/O Unit	4CH	4CH	Input range: 0 to 5 V, 1 to 5 V, 0 to 10 V, ±10 V, 0 to 20	Resolution: 1/12000	CP1W-MAD44	UC1, N, CE	
	4CH	2CH	mA, or 4 to 20 mA. Output range:	Resolution: 1/12000	CP1W-MAD42	001, N, CL	
	2CH	1CH	1 to 5 V, 0 to 10 V, ±10 V, 0 to 20 mA, or 4 to 20 mA.	Resolution: 1/6000	CP1W-MAD11	UC1, N, L, CE	
	2CH		Sensor type: Thermocouple (J or K)		CP1W-TS001		
emperature Sensor Unit	4CH		Sensor type: Thermocouple (J or K)		CP1W-TS002		
	2CH		Sensor type: Platinum resistance therm (Pt100 or JPt100)	ometer	CP1W-TS101	UC1, N, L, CE	
	4CH		Sensor type: Platinum resistance therm (Pt100 or JPt100)	ometer	CP1W-TS102		
	4CH		Sensor type: Thermocouple (J or K) 2 channels can be used as analog input. 1/12000 Input range: 1 to 5 V, 0 to 10 V, 4-20 mA		CP1W-TS003	UC1, N, CE	
CompoBus/S	12CH		Sensor type: Thermocouple (J or K)		CP1W-TS004		
/O Link Unit	8	8	CompoBus/S slave		CP1W-SRT21	UC1, N, L, CE	

■ I/O Connecting Cable

Name	Specifications	Model	Standards
I/O Connecting Cable	80 cm (for CP1W Expansion Units)	CP1W-CN811	UC1, N, L, CE

Note: An I/O Connecting Cable (approx. 6 cm) for horizontal connection is provided with CP1W Expansion Units.

■ Optional Products, Maintenance Products and DIN Track Accessories

Name	Name Specifications		Standards
Battery Set For CP1H CPU Units (Use batteries within two years of manufacture.)		CJ1W-BAT01	CE
	Length: 0.5 m; Height: 7.3 mm	PFP-50N	
DIN Track	Length: 1 m; Height: 7.3 mm	PFP-100N	-
	Length: 1 m; Height: 16 mm	PFP-100N2	
End Plate There are 2 stoppers provided with a CJ Unit Adapter as standard accessories to secure the Units on the DIN Track.		PFP-M	

■ CJ-series Special I/O Units and CPU Bus Units

Category	Name	Specifications	Model	Standard		
CP1H CPU	CJ Unit Adapter	Adapter for connecting CJ-series Special I/O Units and CPU Bus Units (includes CJ-series End Cover and 2 End Plates)	CP1W-EXT01	UC1, N, L,		
Jnit options			CE			
		4 inputs (1 to 5 V (1/10,000), 0 to 10 V (1/20,000), -5 to 5 V (1/20,000), -10 to 10 V (1/40,000), and 4 to 20 mA (1/10,000)) Conversion Period: 20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points	CJ1W-AD042	UC1, CE		
	Analog Input Units	8 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, –10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 µs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	CJ1W-AD081-V1	UC1, N, L,		
		4 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 µs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	CJ1W-AD041-V1	CE		
		4 outputs (1 to 5 V (1/10,000), 0 to 10 V (1/20,000), and -10 to 10 V (1/40,000) Conversion Period: 20 µs/1 point, 25 µs/2 points, 30 µs/3 points, 35 µs/4 points	CJ1W-DA042V	UC1, CE		
		8 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8000, 250 μs/output.)	CJ1W-DA08V	UC1, N, L, CE		
	Analog Output Units	8 outputs (4 to 20 mA) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8,000, 250 µs/ output.)	CJ1W-DA08C	UC1, N, CE		
		4 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	CJ1W-DA041			
		2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	CJ1W-DA021	UC1, N, L, CE		
	Analog I/O Unit	4 inputs, 2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4000; Conversion speed: 1 ms/point max. (Can be set to 1/8,000, 500 µs/point.)	CJ1W-MAD42			
		4 fully universal inputs: Pt100 (3-wire), JPt100 (3-wire), Pt1000 (3-wire), Pt100 (4 wire), K, J, T, E, L, U, N, R, S, B, WRe5-26, PLII, 4 to 20 mA, 0 to 20 mA, 1 to 5 V, 0 to 1.25 V, 0 to 5 V, 0 to 10 V, ±100-mV selectable range, -1.25 to 1.25 V, -5 to 5 V, -10 to 10 V, ±10-V selectable range Potentiometer resolution/conversion speed: 1/256,000 (conversion cycle: 60 ms/4 points), 1/64,000 (conversion cycle: 10 ms/4 points), 1/16,000 (conversion cycle: 5 ms/4 points)	CJ1W-PH41U *	UC1, CE		
	Process Input Units	4 fully universal inputs: Pt100, JPt100, Pt1000, K, J, T, L, R, S, B, 4 to 20 mA, 0 to 20 mA, 1 to 5 V, 0 to 5 V, 0 to 10 V Conversion speed: 250 ms/4 points	CJ1W-AD04U	UC1, L, CE		
		4 inputs, B, J, K, L, R, S, T; Conversion speed: 250 ms/4 inputs	CJ1W-PTS51			
J1 Special O Units		4 inputs, Pt100 Ω (JIS, IEC), JPt100 Ω , Conversion speed: 250 ms/4 inputs	CJ1W-PTS52			
e enne		2 inputs, B, E, J, K, L, N, R, S, T, U, W, Re5-26, PL ±100 mV, Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs	CJ1W-PTS15	UC1, CE		
		2 inputs, 0 to 1.25 V, -1.25 to 1.25 V, 0 to 5 V, 1 to 5 V, -5 to 5 V, 0 to 10 V, -10 to 10 V, ±10-V selectable range, 0 to 20 mA, 4 to 20 mA	CJ1W-PDC15			
		4 loops, thermocouple input, NPN output	CJ1W-TC001			
		4 loops, thermocouple input, PNP output	CJ1W-TC002			
		2 loops, thermocouple input, NPN output, heater burnout detection function	CJ1W-TC003			
		2 loops, thermocouple input, PNP output, heater burnout detection function				
	Temperature Control	4 loops, platinum resistance thermometer input, NPN output	CJ1W-TC101	UC1, N, L,		
	Units	4 loops, platinum resistance thermometer input, PNP output 2 loops, platinum resistance thermometer input, NPN output, heater burnout	CJ1W-TC102	CE		
		detection function 2 loops, platinum resistance thermometer input, PNP output, heater burnout	CJ1W-TC103	-		
		detection function	CJ1W-TC104	UC1, N, L,		
	Unit	High-speed Counter Unit 2 inputs, max. input frequency: 500 kpps				
		Pulse train, open collector output, 1 axis	CJ1W-NC113			
		Pulse train, open collector output, 2 axes	CJ1W-NC213			
	De sitier Os stast Unite	Pulse train, open collector output, 4 axes	CJ1W-NC413			
	Position Control Units	Pulse train, line driver output, 1 axis	CJ1W-NC133	UC1, CE		
		Pulse train, line driver output, 2 axes	CJ1W-NC233	1		
		Pulse train, line driver output, 4 axes	CJ1W-NC433	1		
	Space Unit		CJ1W-SP001	1		
		For V680 Series, 1 R/W Head	CJ1W-V680C11	+		
		For V680 Series, 2 R/W Heads	CJ1W-V680C12	-		
	ID Sensor Units	For V600 Series, 1 R/W Head	CJ1W-V600C11	UC, CE		
		For V600 Series, 2 R/W Heads	CJ1W-V600C12			
	CompoNet Master Unit	Word slaves: 2,048 points, Bit slaves: 512 points	CJ1W-CRM21	U, U1, N, L CE		
	CompoBus/S Master			UC1, N, L,		

* If a CJ1W-PH41U is used, do not use a CP1H CPU Unit with relay contact outputs or Expansion Units with relay contact outputs. **Note:** Refer to the *CJ1 catalog* (Cat. No. P052) for information on the CJ1 Special I/O Units.

CP1H

Category	Name	Specifications	Model	Standards		
	Controller Link Units	Wired (shielded twisted-pair cable)		CJ1W-CLK23	UC1, N, L, CE	
		1 RS-232C port and 1 RS-422A/485 port		CJ1W-SCU42		
		2 RS-232C ports		CJ1W-SCU22	UC1, N, L, CE	
	Serial Communications	2 RS-422A/485 ports		CJ1W-SCU32		
	Units	1 RS-232C port and 1 RS-422A/485 port		CJ1W-SCU41-V1		
CJ1 CPU EtherNet/IP Unit		2 RS-232C ports	CJ1W-SCU21-V1			
		2 RS-422A/485 ports		CJ1W-SCU31-V1		
	EtherNet/IP Unit	Shielded twisted-pair cable (STP), category 5 or Tag data links and message communications sup	CJ1W-EIP21	UC1, N, L, CE		
Bus Units	Ethernet Unit	100Base-TX	CJ1W-ETN21			
	DeviceNet™ Unit	Functions as master and/or slave; allows control master	CJ1W-DRM21			
			2 axes	CJ1W-NC271		
	MECHATROLINK-II	Control commands sent using MECHATROLINK-II synchronized communications	4 axes	CJ1W-NC471	1	
	Position Control Unit	16 axes max., direct operation from ladder	16 axes	CJ1W-NCF71		
		diagram, control modes: position/ speed/torque	16 axes	CJ1W-NCF71-MA	– UC1, CE –	
	FI-net Unit	100Base-TX	,	CJ1W-FLN22		
	SPU	High-speed Data Storage Unit	CJ1W-SPU01-V2	1		

Note: Refer to the CJ1 catalog (Cat. No. P052) for information on the CJ1 CPU Bus Units.

Industrial Switching Hubs

		Specifications			Specifications			Current		
Product name	Appearance	Functions	No. of ports	Failure detection	Accesories	consumption (A)	Model	Standards		
Industrial		Quality of Service (QoS): EtherNet/IP TM control data priority Failure detection:	3	No	Power supply connector	0.22	W4S1-03B	UC, CE		
Switching Hubs	-		5	No		0.22	W4S1-05B			
	Broadcast storm and LSI error detection 10/100BASE-TX, Auto-Negotiation	5	Yes	 Power supply connector Connector for informing error 	0.22	W4S1-05C	CE			

General Specifications

Туре	AC power supply models	DC power supply models			
Item Model	CP1H-□□-A	CP1H-□□-D			
Power supply	100 to 240 VAC 50/60 Hz	24 VDC			
Operating voltage range	85 264 VAC	20.4 to 26.4 VDC (with 4 or more Expansion Units and Expansion I/O Units: 21.6 to 26.4 VDC)			
Power consumption	100 VA max. (CP1H-00-A)(page 28)	50 W max. (CP1H-□□-D)(page 28)			
Inrush current (See note.)	100 to 120 VAC inputs: 20 A max. (for cold start at room temperature) 8 ms max. 200 to 240 VAC inputs: 40 A max. (for cold start at room temperature), 8 ms max.	30 A max. (for cold start at room temperature) 20 ms max.			
External power supply	300 mA at 24 VDC	None			
Insulation resistance	$20\ \text{M}\Omega$ min. (at 500 VDC) between the external AC terminals and GR terminals	No insulation between primary and secondary for DC power supply			
Dielectric strength	2,300 VAC at 50/60 Hz for 1 min between the external AC and GR terminals, leakage current: 5 mA max.	No insulation between primary and secondary for DC power supply			
Noise immunity	Conforms to IEC 61000-4-4. 2 kV (power supply line)				
Vibration resistance	Conforms to JIS C60068-2-6. 10 to 57 Hz, 0.075-mm amplitude, 80 minutes each. Sweep time: 8 minutes \times 10 sweeps = total tim	57 to 150 Hz, acceleration: 9.8 m/s 2 in X, Y, and Z directions for ue of 80 minutes)			
Shock resistance	Conforms to JIS C60068-2-27. 147 m/s ² three times each in X, Y	/, and Z directions			
Ambient operating tempera- ture	0 to 55°C				
Ambient humidity	10% to 90% (with no condensation)				
Ambient operating environ- ment	No corrosive gas				
Ambient storage temperature	-20 to 75°C (Excluding battery.)				
Power holding time	10 ms min.	2 ms min.			

Note: The above values are for a cold start at room temperature for an AC power supply, and for a cold start for a DC power supply.

• A thermistor (with low-temperature current suppression characteristics) is used in the inrush current control circuitry for the AC power supply. The thermistor will not be sufficiently cooled if the ambient temperature is high or if a hot start is performed when the power supply has been OFF for only a short time. In those cases the inrush current values may be higher (as much as two times higher) than those shown above. Always allow for this when selecting fuses and breakers for external circuits.

A capacitor charge-type delay circuit is used in the inrush current control circuitry for the DC power supply. The capacitor will not be charged if a hot start is
performed when the power supply has been OFF for only a short time, so in those cases the inrush current values may be higher (as much as two times
higher) than those shown above.

Performance Specifications

	Туре	CP1H-XA CPU Units	CP1H-X CPU Units	CP1H-Y CPU Units
Item	Models			CP1H-Y
Control met		Stored program method		
I/O control r		Cyclic scan with immediate refresh	ing	
Program lar		Ladder diagram	ing	
		°	definitions: 128 Maximum number	of instances: 256
Function blo	ocks		definitions: Ladder diagrams, struc	
Instruction	enath	1 to 7 steps per instruction		
Instructions	-	Approx. 500 (function codes: 3 dig	its)	
Instruction	execution time	Basic instructions: 0.10 µs min. Sp	,	
Common pr	ocessing time	0.7 ms	•	
Program ca	pacity	20K steps		
Number of t		288 (32 cyclic tasks and 256 interr	upt tasks)	
	Scheduled	1 (interrupt task No. 2, fixed)	· ·	
	interrupt tasks			1
	Input interrupt	8 (interrupt task No. 140 to 147, fix	,	6 (interrupt task No. 140 to 145, fixed)
	tasks	· · ·	ed and executed for high-speed cou	inter interrupts.)
	ubroutine number	256		
Maximum ju	mp number	256		
	Input bits	272bits (17 words) : CIO 0.00 to 16		
	Output bits	272bits (17 words) : CIO 100.00 to	116.16	
I/O areas	Built-in Analog Inputs	CIO 200 to CIO 203		
(See note.)	Built-in Analog Outputs	CIO 210 to CIO 211		
	Serial PLC Link Area	1,440 bits (90 words): CIO 3100.00	0 to CIO 3189.15 (CIO 3100 to CIO	3189)
Work bits			V511.15 (W0 to W511) s): CIO 3800.00 to CIO 6143.15 (CI	O 3800 to CIO 6143)
TR Area		16 bits: TR0 to TR15		
Holding Are	a	8,192 bits (512 words): H0.00 to H	· · · · · ·	
AR Area			bits (448 words): A0.00 to A447.15 : A448.00 to A959.15 (A448 to A959	
Timers		4,096 bits: T0 to T4095		
Counters		4,096 bits: C0 to C4095		
DM Area		32 Kwords: D0 to D32767		
Data Regist	er Area	16 registers (16 bits): DR0 to DR1	5	
Index Regis	ter Area	16 registers (32 bits): IR0 to IR15		
Task Flag A	rea	32 flags (32 bits): TK0000 to TK00	31	
Trace Memo	ory	4,000 words (500 samples for the	trace data maximum of 31 bits and 6	6 words.)
Memory Cas	ssette	A special Memory Cassette (CP1V Note: Can be used for program bac	,	
Clock functi	on		iation): -4.5 min to -0.5 min (ambie	
Sicon functi			perature: 25° C), -2.5 min to +1.5 m	
			.1): For connecting Support Softwar	
Communica	tions functions		ications Option Boards can be mou	
		A maximum of two Ethernet Option mounted.	Boards can be mounted. When usi	ng CP1W-CIF41 Ver.1.0, one Ethernet Option Board can be
Memory bac	kup	Flash memory: User programs, par memory as initial values.	rameters (such as the PLC Setup), c	comment data, and the entire DM Area can be saved to flash
Battery serv	rice life		nent battery within two years of man	, , , , ,
Built-in inpu		40 (24 inputs, 16 outputs)	······································	20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW
Number of c Expansion (CP Expansion I/O Units: 7 max.; C	J-series Special I/O Units or CPU E	
	er of I/O points	320 (40 built in + 40 per Expansior	n (I/O) Unit \times 7 Units)	300 (20 built in + 40 per Expansion (I/O) Unit × 7 Units)
Interrupt inp		8 inputs (Shared by the external in the quick-response inputs.)		6 inputs (Shared by the external interrupt inputs (counter mode) and the guick-response inputs.)
Interrupt inp	out counter mode	8 inputs (Response frequency: 5 k 16 bits	Hz max. for all interrupt inputs),	6 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits
Quick rear	neo incuto	Up or down counters		Up or down counters
Quick-respo		8 points (Min. input pulse width: 50	ι με max.)	6 points (Min. input pulse width: 50 μs max.)
Scheduled i	merrupts	1		

	Туре	CP1H-XA CPU Units	CP1H-X CPU Units	CP1H-Y CPU Units
Item	Models	CP1H-XA	CP1H-X	CP1H-Y
High-speed cour	nters	100 kHz Value range: 32 bits, Line	direction, up/down, increment),	 2 inputs: Differential phases (4x), 500 kHz or Single-phase, 1 MHz and 2 inputs: Differential phases (4x), 50 kHz or Single-phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range comparison
Pulse outputs Pulse out- (models with puts transistor out- puts only)		Trapezoidal or S-curve acceleratior (Duty ratio: 50% fixed) 4 outputs, 1 Hz to 100 kHz (CCW/0		Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fixed) 2 outputs, 1 Hz to 1 MHz (CCW/CW or pulse plus direction) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction)
	PWM out- puts	Duty ratio: 0.0% to 100.0% (Unit: 0 2 outputs, 0.1 to 6553.5 Hz (Accura		
Built-in analog I/	O terminals	4 analog inputs and 2 analog outputs	None	
Analog control		1 (Setting range: 0 to 255)		
External analog	input	1 input (Resolution: 1/256, Input ra	nge: 0 to 10 V), not isolated	

Note: The memory areas for CJ-series Special I/O Units and CPU Bus Units are allocated at the same as for the CJ-series. For details, refer to the CJ Series catalog (Cat. No. P052).

Built-in Inputs / Built-in Outputs

Terminal Block Arrangement

● CP1H-XA and X CPU Units with AC Power Supply

 LCIO 0
 ICIO 1
 ICIO 1

4	÷	C	10	0)1	0	12	03	3	0	4	0	6	0	00	0	1	C)3	0	04	0	16	٠	(Output
•	-		CC	M	CO	М	CO	м	CC	М	05	5	07	7	CC	M	02	2	CC	M	05	5	0	7	terminals)
			CIO	100											CIO	101									

Built-in Input Area

• CP1H-XA and X CPU Units

PLC Se	etup		Input operati	ion	High-speed counter operation	Pulse output origin search function set to be used.
		Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
CIO 0	00	Normal input 0	Interrupt input 0	Quick-response input 0		Pulse 0: Origin input signal
	01	Normal input 1	Interrupt input 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	Pulse 0: Origin proximity input signal
	02	Normal input 2	Interrupt input 2	Quick-response input 2	High-speed counter 1 (phase-Z/reset)	Pulse output 1: Origin input signal
	03	Normal input 3	Interrupt input 3	Quick-response input 3	High-speed counter 0 (phase-Z/reset)	Pulse output 1: Origin proximity input signal
	04	Normal input 4			High-speed counter 2 (phase-A, increment, or count input)	
	05	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	
	06	Normal input 6			High-speed counter 1 (phase-A, increment, or count input)	
	07	Normal input 7			High-speed counter 1 (phase-B, decrement, or direction input)	
	08	Normal input 8			High-speed counter 0 (phase-A, increment, or count input)	
	09	Normal input 9			High-speed counter 0 (phase-B, decrement, or direction input)	
	10	Normal input 10			High-speed counter 3 (phase-A, increment, or count input)	
	11	Normal input 11			High-speed counter 3 (phase-B, decrement, or direction input)	
CIO 1	00	Normal input 12	Interrupt input 4	Quick-response input 4	High-speed counter 3 (phase-Z/reset)	Pulse output 2: Origin input signal
	01	Normal input 13	Interrupt input 5	Quick-response input 5		Pulse output 2: Origin proximity input signal
	02	Normal input 14	Interrupt input 6	Quick-response input 6		Pulse output 3: Origin input signal
	03	Normal input 15	Interrupt input 7	Quick-response input 7		Pulse output 3: Origin proximity input signal
	04	Normal input 16				
	05	Normal input 17				
	06	Normal input 18				
	07	Normal input 19				
	08	Normal input 20				
	09	Normal input 21				
	10	Normal input 22				
	11	Normal input 23				

Built-in Output Area

• CP1H-XA and CP1H-X CPU Units

	truc- ions	When the instructions to the right are not executed		output instruction e, or ORG) is executed	When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
PLC S	Sotun	Normal outputs		Fixed duty ratio p	ulse outputs	Variable duty ratio pulse output
FLOC	berup	Normal outputs	CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
CIO	00	Normal output 0	Pulse output 0 (CW)	Pulse output 0 (pulse)		
100	01	Normal output 1	Pulse output 0 (CCW)	Pulse output 1 (pulse)		
	02	Normal output 2	Pulse output 1 (CW)	Pulse output 0 (direction)		
	03	Normal output 3	Pulse output 1 (CCW)	Pulse output 1 (direction)		
	04	Normal output 4	Pulse output 2 (CW)	Pulse output 2 (pulse)		
	05	Normal output 5	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	06	Normal output 6	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	07	Normal output 7	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO	00	Normal output 8				PWM output 0
101	01	Normal output 9				PWM output 1
	02	Normal output 10			Origin search 0 (Error counter reset output)	
	03	Normal output 11			Origin search 1 (Error counter reset output)	
	04	Normal output 12			Origin search 2 (Error counter reset output)	
	05	Normal output 13			Origin search 3 (Error counter reset output)	
CIO	06	Normal output 14				
101	07	Normal output 15				

• CP1H-XA and X CPU Units with DC Power supply

)									ICI	01										
+	⊣⊦	-	CO	M	01	03	3	05	07	0	9	11		01	03	3	05	5	07	0	9	11		(Input
•	NC	(Ð	00	0	12	04	(16	08	10		00	1)2	0	4	06		08	10	0	•	terminals)

I	N	с	C	00	0)1	0	12	0	13	0	4	C	6	0	10	C	11	C	13	0	4	C	16	•	(Output
	٠	N		со	М	со	M	CO	М	CC	M	05	5	07	_	СС		02	2	CC	м	0	5	0	7	terminals)
				CIO	100											CIO	101									

Terminal Block Arrangement

• CP1H-Y CPU Units

					-	ne-u	inver in	iputa														
			ícou	JNT	ER						icio	0					CIO 1					
+	⊣⊢	-	AO)+	B0+	ZC	I+ A	1+	B1+	Z1+	CO	M 0	1	05	11		01	0	3	0	5	(1
•	NC	(€	A	0-	B0-	Z0-	A1	- B	1- Z	1.	00	04	1	10	0	0 (02	0	4	٠	(Input terminals)

•

Note: Supply 24 VDC to the bottom 24 VDC input terminals when using bits 04 to 07 of output word CIO 100.

Built-in Input Area

• CP1H-Y CPU Units

PLC	Setup		Input operation	setting	High-speed counter operation setting	Pulse output origin search function set to be used.
		Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
A	۹0				High-speed counter 0 (phase-A,	
					increment, or count input) fixed	
	30				High-speed counter 0 (phase-B, decrement, or direction input) fixed	
Z	ZO				High-speed counter 0 (phase-Z/reset) fixed	Pulse 0: Origin input signal (line driver)
A	\1				High-speed counter 1 (phase-A, increment, or count input) fixed	
E	31				High-speed counter 1 (phase-B, decrement, or direction input) fixed	
Z	Z1				High-speed counter 1 (phase-Z/reset) fixed	Pulse 1: Origin input signal (line driver)
CIO 0	Bit 00	Normal input 0	Interrupt 0	Quick-response input 0		Pulse 2: Origin proximity input signal
	Bit 01	Normal input 1	Interrupt 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	
	Bit 04	Normal input 2			High-speed counter 2 (phase-A, increment, or count input)	
	Bit 05	Normal input 3			High-speed counter 2 (phase-B, decrement, or direction input)	
	Bit 10	Normal input 4			High-speed counter 3 (phase-A, increment, or count input)	
	Bit 11	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	Pulse 3: Origin proximity input signal
CIO 1	Bit 00	Normal input 6	Interrupt 2	Quick-response input 2	High-speed counter 2 (phase-Z/reset)	Pulse 3: Origin input signal
	Bit 01	Normal input 7	Interrupt 3	Quick-response input 3		Pulse 2: Origin input signal
	Bit 02	Normal input 8	Interrupt 4	Quick-response input 4		Pulse 1: Origin input signal (open collector)
	Bit 03	Normal input 9	Interrupt 5	Quick-response input 5		Pulse 0: Origin input signal (open collector)
	Bit 04	Normal input 10				Pulse 1: Origin proximity input signal
	Bit 05	Normal input 11				Pulse 0: Origin proximity input signal

These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

■ Built-in Output Area

• CP1H-Y CPU Units

Instr	uctions	When the instructions to the right are not executed		output instruction , or ORG) is executed	When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
	Setup	Normal output		Fixed duty ratio p	oulse output	Variable duty ratio pulse output
FLU	, Setup	Normai output	CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
C/	W0	Not supported.	Pulse output 0 (CW) fixed	Pulse output 0 (pulse) fixed		
СС	WO	Not supported.	Pulse output 0 (CCW) fixed	Pulse output 1 (pulse) fixed		
C/	W1	Not supported.	Pulse output 1 (CW) fixed	Pulse output 0 (direction) fixed		
СС	W1	Not supported.	Pulse output 1 (CCW) fixed	Pulse output 1 (direction) fixed		
CIO	Bit 04	100.04	Pulse output 2 (CW)	Pulse output 2 (pulse)		
100	Bit 05	100.05	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	Bit 06	100.06	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	Bit 07	100.07	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO	Bit 00	101.00			Origin search 2 (Error counter reset output)	PWM output 0
101	Bit 01	101.01			Origin search 3 (Error counter reset output)	PWM output 1
	Bit 02	101.02			Origin search 0 (Error counter reset output)	
	Bit 03	101.03			Origin search 1 (Error counter reset output)	

These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

CP1H I/O Specifications for CPU Units

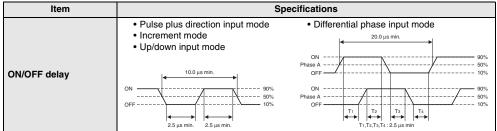
Input Specifications

		Specifications	
ITEM	High-speed counter inputs (phases A and B)	Interrupt inputs and quick-response inputs	Normal inputs
CP1H-XA/X CPU Units	CIO 0.04 to CIO 0.11	CIO 0.00 to CIO 0.03 and CIO 1.00 to CIO 1.03	CIO 1.04 to CIO 1.11
CP1H-Y CPU Units	CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11	CIO 0.00, CIO 0.01 and CIO 1.00 to CIO 1.03	CIO 1.04, CIO 1.05
Input voltage	24 VDC +10%/-15%		
Applicable sensors	2-wire sensors or 3-wire sensors		
Input impedance	3.0 kΩ		4.7 kΩ
Input current	7.5 mA typical		5 mA typical
ON voltage	17.0 VDC min.		14.4 VDC min.
OFF voltage/current	1 mA max. at 5.0 VDC		
ON delay	2.5 μs max.	50 μs max.	1 ms max.
OFF delay	2.5 μs max.	50 μs max.	1 ms max.
Circuit configuration	Input LED Input LED Input LED Internal crouts	Input LED Input LED Internal com	Input LED

High-speed Counter Function Input Specifications

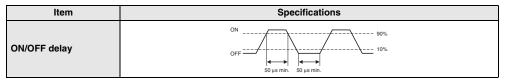
CP1H-XA/X CPU Units (Input bits: CIO 0.04 to CIO 0.11)

CP1H-Y CPU Units (Input bits: CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11)



Interrupt Input Counter Mode

CP1H-XA/X CPU Units (Input bits: CIO 0.00 to CIO 0.03, CIO 1.00 to CIO 1.03) CP1H-Y CPU Units (Input bits: CIO 0.00, CIO 0.11, CIO 1.00 to CIO 1.03)



• High-speed Counter Inputs (Line-driver Inputs)

CP1H-Y CPU Units

Item	Specifications	
High-speed counter in- puts	Phases A and B	Phase Z
Input voltage	RS-422A line-driver, AM26LS31 or equivalent Note: The power supply voltage on the line-driver must be 5 V \pm 5% max.	
Input type	Line-driver input	
Input current	10 mA typical	13 mA typical
Circuit configuration	+ 330 Ω 680 Ω \$330 pF + * * D- Internal circuits 330 Ω	$+ \underbrace{180 \Omega}_{560 \Omega} \underbrace{6600 \text{ pF}}_{180 \Omega} + \underbrace{-}_{180 \Omega} \underbrace{-}_{180 \Omega} \underbrace{+}_{180 \Omega$
ON/OFF delay	 Pulse plus direction input mode Increment mode Up/down input mode ON OFF OFF Differential phase input mode Differentinput mode Differentinput mode Differ	ON Phase Z OFF

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Output Specifications

• CPU Units with Relay Outputs

	Item		Specifications
Max. s	witching	g capacity	2 A, 250 VAC (cos = 1), 2 A, 24 VDC 4 A/common)
Min. sv	witching	capacity	5 VDC, 10 mA
Ser-	Elec- Resis-		100,000 operations (24 VDC)
vice life of relay	trical	Induc- tive load	48,000 operations (250 VAC, cos
,	Mechanical		20,000,000 operations
ON de	lay		15 ms max.
OFF de	elay		15 ms max.
Circuit	Circuit configuration		Output LED Output LED Internal circuits COM Maximum 250 VAC: 2 A, 24 VDC: 2 A

• CPU Units with Transistor Outputs (Sinking/Sourcing)

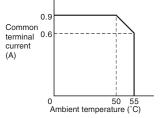
Item	Specifications				
CP1H-XA/X CPU Units	CIO 100.00 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02 to CIO 101.07		
CP1H-Y CPU Units	CIO 100.04 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02, CIO 101.03		
Max. switching capacity	4.5 to 30 VDC: 300 mA/point, 0.9 A/common, 3.6 A/Unit *1*2				
Min. switching capacity	4.5 to 30 VDC, 1 mA				
Leakage current	0.1 mA max.				
Residual voltage	0.6 V max.	1.5 V max.			
ON delay	0.1 ms max.				
OFF delay	0.1 ms max.		1 ms max.		
Fuse	1/common *3				
Circuit configuration	Sinking Outputs	Sourcing Outputs	OUT OUT COM (-) 4.5 to 30 VDC COM (-) 4.5 to 30 VDC 4.5 to 30 VDC		

Note: 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.

Also do not exceed 0.9 A for the total for CIO 100.00 to CIO 100.03. (CIO 100.00 to CIO 100.03 is different common.) *1

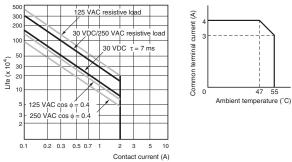
*2 *3 A maximum of 0.9 A per common can be switched at an ambient temperature of 50 $^\circ\text{C}.$

Fuses cannot be replaced by the user.



Note: Under the worst conditions, the service life of output contacts is as showr

on the left. The service life of relays is as shown in the following diagram as a guide line.



Pulse outputs

CP1H-XA/X CPU Units: Output bits CIO 100.00 to CIO 100.07 CP1H-Y CPU Units: Output bits CIO100.04 to CIO 100.07

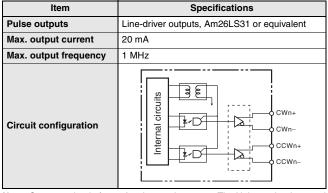
Item	Specifications
Max. switching capacity	30 mA at 4.75 to 26.4 VDC
Min. switching capacity	7 mA at 4.75 to 26.4 VDC
Max. output frequency	100 kHz
Output waveform	OFF 90%

Note: 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.

- 2. The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance. 3. The OFF and ON refer to the output transistor. The output transistor is ON
- at level "L".

Pulse Outputs (Line-driver Outputs)

CP1H-Y CPU Units



Note: Connect a load of 20 mA or less to the output. The Unit may be damaged if a current of more than 20 mA is output.

■ Analog I/O Specifications (CP1H-XA CPU Units Only)

	Item	Voltage I/O Current I/O			
	Number of analog inputs	4			
	Input signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, or -10 to 10 V	0 to 20 mA or 4 to 20 mA		
	Max. rated input	±15 V	±30 mA		
	External input impedance	1 MΩ min.	Approx. 250 Ω		
Analog Input	Resolution	1/6,000 or 1/12,000 (full scale)			
Section	Overall accuracy	25°C: ±0.3% full scale/0 to 55°C: ±0.6% full scale	25°C: $\pm 0.4\%$ full scale/0 to 55°C: $\pm 0.8\%$ full scale		
	A/D conversion data	Full scale for -10 to 10 V: F448 (E890) to 0BB8 (1770) hex Full scale for other ranges: 0000 to 1770 (2EE0) hex			
	Averaging	Supported (Set for individual inputs in the PLC Setup.)			
	Open-circuit detection	Supported (Value when disconnected: 8000 Hex)			
	Number of outputs	2			
	Output signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, -10 to 10 V	0 to 20 mA or 4 to 20 mA		
Analog	Allowable external output load resistance	1 kΩ min.	600 Ω max.		
Output	External output impedance	0.5 Ω max.			
Section	Resolution	1/6000 or 1/12000 (full scale)			
	Overall accuracy	25°C±0.4% of full scale, 0 to 55°C±0.8% of full scale			
	D/A conversion data	Full scale for -10 to 10 V: F448 (E890) to 0BB8 (1770) hex Full scale for other ranges: 0000 to 1770 (2EE0) hex			
Conversi	on time	1 ms/point			
Isolation	method	Photocoupler isolation between analog I/O terminals and internal circuits. No isolation between analog I/O signals.			

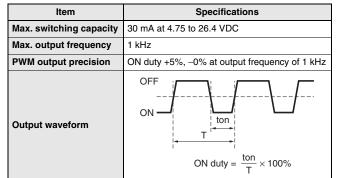
Built-in Analog Input Switch (Factory Settings)



Voltage input

Pulse outputs

CP1H-XA/X/Y CPU Units: Output bits CIO101.00, CIO 101.01



Note: 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.

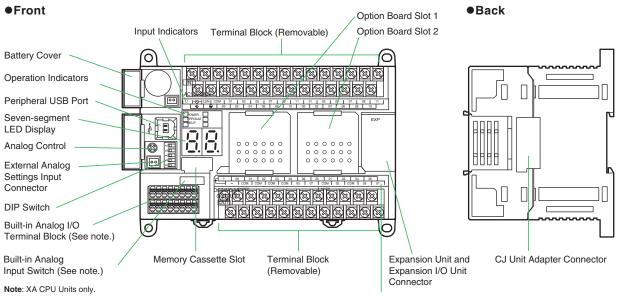
- $\ensuremath{\mathbf{2}}.$ The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance.
- ${\bf 3.}\,$ The OFF and ON refer to the output transistor. The output transistor is ON at level "L".

Built-in Analog I/O Terminal Block Arrangement

AD1+	AD1-	AD2+	AD2-	AD3+	AD3-	AD4+	AD4-
Ο	0	0	0	0	0	0	0
VOUT	I IOUT1	COM1	VOUT2	IOUT2	COM2	AG	AG
				\cap		\cap	

External Interfaces

CPU Unit Nomenclature



Output Indicators

Option Unit Specifications

■ Serial Communications Specifications (CP1W-CIF01/-CIF11)

Item	Function	Interface
Peripheral USB port	For connecting Peripheral Device.	Conforms to USB 1.1, B-type connector
Serial port 1 (Option board slot 1)	Host Link, No-protocol, NT Link (1: N), Serial PLC Link (See note.),	The following can be used for either port. CP1W-CIF01 RS-232C Option Board
Serial port 2 (Option board slot 2)	Serial Gateway (CompoWay/F master, Modbus-RTU master), Modbus-RTU easy master function, ToolBus	RS-422A/485 Option Board (Maximum transmission distance 50m) CP1W-CIF12 RS-422A/485(Isolated-type) Option Board (Maximum transmission distance 500m) Can be used with either port.

Note: Serial PLC Link can be used with either serial port 1 or serial port 2.

■ Ethernet Communications Specifications (CP1W-CIF41)

Item			Specifications
Applicable	Applicable PLCs		CP1H CPU Units
Number of	Number of Units that can be mounted		2 sets. (The CP1W-CIF41 Ver.1.0 and Ver.2.0 can be combined and used with one CPU Unit. When using CP1W-CIF41 Ver.1.0, only one unit can be mounted in an option board slot.)
	Media access method		CSMA/CD
	Modulation method		Baseband
	Transmission paths		Star form
	Baud rate		100 Mbit/s (100Base-TX), 10 Mbit/s (10Base-T)
Transfer	Transmission media	100 Mbit/s	 Unshielded twisted-pair (UDP) cable Categories: 5, 5e Shielded twisted-pair (STP) cable Categories: 100 Ω at 5, 5e
		10 Mbit/s	 Unshielded twisted-pair (UDP) cable Categories: 3, 4, 5, 5e Shielded twisted-pair (STP) cable Categories: 100 Ω at 3, 4, 5, 5e
	Transmission Distance		100 m (distance between hub and node)

Item		FINS Communications Service Specifications
Number of nodes		254
Message length		1016 bytes max.
Size of buffer		8k
Communicatio	ons Function	FINS Communications Service (UDP/IP, TCP/IP)
	Protocol used	UDP/IP
FINS/UDP	Server/Client	Only server (Cannot be used as a client)
method	Port number	9600 (default) Can be changed.
	Protection	No
	Protocol used	TCP/IP
	Server/Client	Only server (Cannot be used as a client)
FINS/TCP method	Number of connections	Up to 2 simultaneous connections and only one connection can be set to client
	Port number	9600 (default) Can be changed.
	Protection	Yes (Specification of client IP addresses when unit is used as a server)

Note: 1. CX-Programmer version 8.1 or higher (CX-One version 3.1 or higher) is required.
2. Use CX-Integrator version 2.33 or higher (CX-One version 3.1 or higher) when the system needs to be set the routing tables. However, CX-Integrator does not support the other functions, using CP1W-CIF41, such as transferring the parameters and network structure.
3. To connect the CP1H CPUs with the NS-series Programmable Terminals via Ethernet using CP1W-CIF41, make sure that the system version of NS Series is 8.0 or binder.

is 8.2 or higher.

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■ LDC Option Board (CP1W-DAM01) ● Specifications

Item	Function
Mounting port	CP1H: Option board slot 1
Communications protocol	Peripheral bus (Turn ON DIP switch pin 4.)
Number of display characters	4 rows × 12 characters: 48 characters max.
Display characters	5×7 dots (alphanumeric and symbols).
Backlight	Electroluminescence (EL): Normal: Lit green; Error: Flashing red

LCD Functions

Operation		Description				
Changing operating modes		Change the PLC operating mode without using the CX-Programmer.				
I/O memory		Read and change the present values in the m	nemory areas and force-set or force-reset bits.			
PLC Setup of	operations	Read and change the PLC Setup.				
Analog I/O n	nonitor	Monitor the analog adjustment and present va	alue for the external analog setting input.			
Error log display		Read the log of errors that have occurred.				
Memory cassette operation		Transfer and verify user programs between th	e PLC and memory cassette.			
User monito	or settings	Read the status of up to 16 words and bits wi	th comments. You can use this setting to read data on the startup display.			
Message dis settings	splay function	Display a user-set message of up to 48 chara A maximum of 16 screens can be registered to	acters on the LCD Option Board when a specified bit turns ON. for display.			
		(Operation:			
Timers	Day timer	Use this timer for ON/OFF switching at a specified times every day from the starting day of the week to the ending day of the week. Sixteen timers cam be set from timer 01 to timer 16.	Starting day of the week Example: Monday ON OFF Starting time Example: 9:00 Ending day of the week Example: Friday The starting time Ending time Starting time Ending time Starting time Ending time Starting ti			
	Weekly timer	Use this timer for ON/OFF operation in intervals of one week that starts one day and ends another day. Sixteen timers cam be set from timer No. 01 to timer No. 16.	Operation: Starting day of the week Example: Monday ON OFF OFF t Starting time Example: 12:00 Example: 8:00 t t t t t t t t t t t t t			
	Calendar timer	Use the calendar timers for ON or OFF operation in intervals of one year from the starting day to the ending day. Sixteen timers can be set from timer 01 to timer 16.	Operation: OFF OFF Starting day Ending day Set September 1 July 1 August 31 as the ending day.			
Saving setting		Save the various settings that you set with the saved in the PLC to the LCD Option Board.	e LCD Option Board to the DM Area of the PLC. You can also write the settings			
Language		Changing the display language (Japanese/English)				
Other functions		 Setting the time of the PLC's built-in clock Reading system data (e.g., unit version and Setting the backlight lighting time Adjusting LCD contrast Reading cycle time (e.g., average, maximun Clearing data for the LCD Option Board 				

Expansion I/O Unit Specifications

CP1W-40EDR/40EDT/40EDT1/32ER/32ET/32ET1/20EDR1/20EDT/20EDT1/16ER/16ET/16ET1/8ED/8ER/8ET/8ET1 Expansion I/O Units

Expansion I/O Units can be connected to the CPU Unit to configure the required number of I/O points.

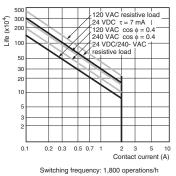
• DC Inputs (CP1W-40EDR/40EDT/40EDT1/20EDR1/20EDT1/20EDT1/8ED)

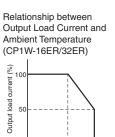
Item	Specifications
Input voltage	24 VDC +10%/-15%
Input impedance	4.7 kΩ
Input current	5 mA typical
ON voltage	14.4 VDC min.
OFF voltage	5.0 VDC max.
ON delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)
OFF delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)
Circuit configuration	Input LED

Relay Outputs (CP1W-40EDR/32ER/20EDR1/16ER/8ER)

	Item		Specifications
Max. switching capacity			2 A, 250 VAC (cos = 1), 24 VDC 4 A/common
Min. swit	ching c	apacity	5 VDC, 10 mA
Service Elec-		Resistive load	150,000 operations (24 VDC)
life of relay	trical	Inductive load	100,000 operations (24 VAC cos = 0.4)
	Mecha	nical	20,000,000 operations
ON delay	/		15 ms max.
OFF dela	ıy		15 ms max.
Circuit configuration		ation	Output LED Internal circuits

- Note: 1. Do not apply a voltage exceeding the rated voltage to an input terminal.
 2. Can be set in the PLC Setup to 0, 0.5, 1, 2, 4, 8, 16 or 32 ms. The CP1W-40EDR/EDT/EDT1 are fixed at 16 ms.
- **Note:** Under the worst conditions, the service life of output contacts is as shown on the left. The service life of relays is as shown in the following diagram as a guideline.





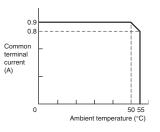


When using the CP1W-32ER, do not allow more than 24 outputs to be ON simultaneously regardless of the ambient temperature.

• Transistor Outputs (Sinking/Sourcing) (CP1W-40EDT/-40EDT1/-32ET/-32ET1/-20EDT/-20EDT1/-16ET/-16ET1/-8ET/-8ET1)

			Specifications				
Item	CP1W-40EDT CP1W-40EDT1	CP1W-32E CP1W-32ET1	CP1W-20EDT CP1W-20EDT1	CP1W-16ET CP1W-16ET1	CP1W-8ET CP1W-8ET1		
Max. switching ca- pacity (See note 3.)	- 4.5 to 30 VDC: 0.3 A/point		24 VAC +10%/ -5%: 0.3 A/point	4.5 to 30 VDC: 0.3 A/point	 OUT00/01 4.5 to 30 VDC, 0.2 A/output OUT02 to 07 4.5 to 30 VDC, 0.3A/output 		
	0.9 A/common 3.6 A/common		0.9 A/common 1.8 A/common	0.9 A/common 3.6 A/common	0.9 A/common 1.8 A/common		
Leakage current	0. 1mA max.						
Residual voltage	1.5 V max.	1.5 V max.					
ON delay	0.1ms max.						
OFF delay	1 ms max. at 24 VDC +10%/-5%, 5 to 300 mA						
Max number of Simultaneously ON Points of Output	16 pts (100%)	24 pts (75%)	8 pts (100%)	16 pts (100%)	8 pts (100%)		
Fuse (See note 2.)	1/common						
	Sinking Outputs		Sourci	ng Outputs			
Circuit configura- tion				24 VDC/ OUT 30 VDC			

- Note: 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.
 2. The fuses cannot be replaced by the
 - user.
 - 3. A maximum of 0.9 A per common can be switched at an ambient temperature of 50°C.



CP1W-AD041/AD042/DA021/DA041/DA042/MAD11/MAD42/MAD44 Analog Units

Analog values that are input are converted to binary data and stored in the input area, or binary data is output as analog values.

Analog Input Units

Model		CP1W	-AD041	CP1W	-AD042	
ltem		Voltage Input	Current Input	Voltage Input	Current Input	
Number of inputs		4 inputs (4 words allocated)	4 inputs (4 words allocated)			
Input signal range		0 to 5 VDC, 1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA	0 to 5 VDC, 1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA	
Max. rated input		±15 V	±30 mA	±15 V	±30 mA	
External input impedance		1 MΩ min.	Approx. 250 Ω	1 M Ω min.	Approx. 250 Ω	
Resolution		1/6000 (full scale)		1/12000 (full scale)		
Overall accuracy	25°C	0.3% full scale	0.4% full scale	0.2% full scale	0.3% full scale	
Overall accuracy	0 to 55°C	0.6% full scale	0.8% full scale	0.5% full scale	0.7% full scale	
A/D conversion data		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: F448 to 0BB8 Hex Full scale for other ranges: 0000 to 1770 Hex		16-bit binary (4-digit hexadecimal) Full scale for –10 to 10 V: E890 to 1770 Hex Full scale for other ranges: 0000 to 2EE0 Hex		
Averaging function		Supported (Set in output words n+1 and n+2.)				
Open-circuit detection function		Supported				
Conversion time		2 ms/point (8 ms/all points)		1 ms/point (4 ms/all points)		
Isolation method		Photocoupler isolation betwee	een analog I/O terminals and	internal circuits. No isolation b	etween analog I/O signals.	
Current consumption		5 VDC: 100 mA max.; 24 VE	DC: 90 mA max.	5 VDC: 100 mA max.; 24 VI	DC: 50 mA max.	

Analog Output Units

	Model		CP1W-DA021	/CP1W-DA041	CP1W	-DA042
Item		Voltage Output	Current Output	Voltage Output	Current Output	
	Number of outputs		CP1W-DA021: 2 outputs (2 words allocated) CP1W-DA041: 4 outputs (4 words allocated) 4		4 outputs (4 words allocated)
	Output signal range		1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA	1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA
Analog	External out load resista	put allowable	2 kΩ min.	350 Ω max.	2 kΩ min.	350 Ω max.
output	External output impedance		0.5 Ω max.		0.5 Ω max.	
section	ection Resolution		1/6000 (full scale)		1/12000 (full scale)	
	Overall 25°C	25°C	0.4% full scale		0.3% full scale	
	accuracy	0 to 55°C	0.8% full scale		0.7% full scale	
	D/A convers	ion data	16-bit binary (4-digit hexaded Full scale for -10 to 10 V: F4 Full scale for other ranges: 0	48 to 0BB8 Hex	16-bit binary (4-digit hexadecimal) Full scale for -10 to 10 V: E890 to 1770 Hex Full scale for other ranges: 0000 to 2EE0 Hex	
Conversion time		CP1W-DA021: 2 ms/point (4 ms/all points) CP1W-DA041: 2 ms/point (8 ms/all points)		1 ms/point (4 ms/all points)		
Isolation method		Photocoupler isolation between analog I/O terminals and internal circuits. No isolation between analog I/O sig			etween analog I/O signals.	
Current cons	sumption		CP1W-DA021: 5 VDC: 40 m/ CP1W-DA041: 5 VDC: 80 mA		5 VDC: 70 mA max.; 24 VDC	C: 160 mA max.

Analog I/O Units

	Model		CP1W-MAD42	/CP1W-MAD44	CP1W	-MAD11
	Item		Voltage I/O	Current I/O	Voltage I/O	Current I/O
	Number of inputs		4 inputs (4 words allo	cated)	2 inputs (2 words allo	cated)
	Input signal range Max. rated input		0 to 5 VDC, 1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA	0 to 5 VDC, 1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA
			±15 V	±30 mA	±15 V	±30 mA
	External input impedance		1 MΩ min.	Approx. 250 Ω	1 M Ω min.	Approx. 250 Ω
Analog Input	Resolution		1/12000 (full scale)		1/6000 (full scale)	
Section	Overall accuracy	25°C	0.2% full scale	0.3% full scale	0.3% full scale	0.4% full scale
	Overall accuracy	0 to 55°C	0.5% full scale	0.7% full scale	0.6% full scale	0.8% full scale
	A/D conversion data		Full scale for -10 to 1	bit binary (4-digit hexadecimal) Il scale for -10 to 10 V: E890 to 1770 hex I scale for other ranges: 0000 to 2EE0 hex Full scale for other ranges: 0000 to 2EE0 hex		0 V: F448 to 0BB8 hex
	Averaging function		Supported		Supported (Settable for individual input DIP switch)	
	Open-circuit detection function		Supported			
	Number of outputs		CP1W-MAD42: 2 outpu CP1W-MAD44: 4 outpu		1 output (1 word allocated)	
	Output signal range		1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA	1 to 5 VDC, 0 to 10 VDC, or -10 to 10 VDC	0 to 20 mA or 4 to 20 mA
	Allowable external output lo	ad resistance	2 kΩ min.	350 Ω max.	1 kΩ min.	600 Ω max.
Analog Output Section	External output impedance		0.5 Ω max.		0.5 Ω max.	
Section	Resolution		1/12000 (full scale)		1/6000 (full scale)	
	Overall accuracy	25°C	0.3% full scale		0.4% full scale	
	Overall accuracy	0 to 55°C	0.7% full scale		0.8% full scale	
	Set data (D/A conversion)		Full scale for -10 to 10 V: E890 to 1770 hex Full scale for -10			nexadecimal) 0 V: F448 to 0BB8 hex nges: 0000 to 1770 hex
Conversion time		CP1W-MAD42: 1 ms/point (6 ms/all points) CP1W-MAD44: 1 ms/point (8 ms/all points)		2 ms/point (6 ms/all points)		
Isolation method			Photocoupler isolation between analog I/O terminals and internal circuits. No isolation between analog I/O signals.		cuits.	
Current consumption			CP1W-MAD42: 5 VD0 VDC: 120 mA max. CP1W-MAD44: 5 VD0 VDC: 170 mA max.	,	5 VDC: 83 mA max.,	24 VDC: 110 mA max.

■ Temperature Sensor Units: CP1W-TS001/TS002/TS003/TS004/TS101/TS102

By mounting a Temperature Sensor Unit to the PLC, inputs can be obtained from thermocouples or platinum resistance thermometers, and temperature measurements can be converted to binary data (4-digit hexadecimal) and stored in the input area of the CPU Unit.

ltem	CP1W-TS001	CP1W-TS002	CP1W-TS101	CP1W-TS102	
item	Thermocouples		Platinum resistance thermometer		
Temperature sensors			Switchable between Pt100 and JPt100, but same type m be used for all inputs.		
Number of inputs	2	4	2	4	
Allocated input words	2	4	2	4	
Accuracy	(The larger of ±0.5% of conver max. *	rted value or $\pm 2^{\circ}$ C) ± 1 digit	(The larger of $\pm 0.5\%$ of converted value or $\pm 1^\circ C) \pm 1$ digit max.		
Conversion time	250 ms for 2 or 4 input points				
Converted temperature data	16-bit binary data (4-digit hexadecimal)				
Isolation	Photocouplers between all temperature input signals				
Current consumption	5 VDC: 40 mA max., 24 VDC: 59 mA max. 5 VDC: 54 mA max., 24 VDC: 73 mA max.				

* Accuracy for a K-type sensor at -100°C or less is \pm 4°C \pm 1 digit max.

The rotary switch is used to set the temperature range.

Setting		CP1W-TS001/TS002			CP1W-TS101/TS102		
		Input type	Range (°C)	Range (°F)	Input type	Range (°C)	Range (°F)
	0	V	-200 to 1,300	-300 to 2,300	Pt100	-200.0 to 650.0	-300.0 to 1,200.0
	1		0.0 to 500.0	0.0 to 900.0	JPt100	-200.0 to 650.0	-300.0 to 1,200.0
	2	1	-100 to 850	-100 to 1,500			
	3	5	0.0 to 400.0	0.0 to 750.0		Cannot be set.	
	4 to F		Cannot be set.				

Main Specifications

Ite	em	CP1W-TS003	
Temperature sensors		Thermocouples or analog input *1	
		Switchable between K and J, but same type must be used for all inputs.	
Number of inputs		Thermocouples inputs :4 , Analog inputs :2 Two analog inputs can be shared with thermocouples inputs.	
	Thermocouple inputs	(The larger of $\pm 0.5\%$ of converted value or $\pm 2^{\circ}$ C) ± 1 digit max. *2	
Accuracy at 25°C	Analog voltage inputs	0.5% full scale	
	Analog inputs	0.6% full scale	
	Thermocouple inputs	(The larger of $\pm 1\%$ of converted value or $\pm 4^{\circ}$ C) ± 1 digit max. *3	
Accuracy at 0 to 55°C	Analog voltage inputs	1.0 % full scale	
Analog inputs		1.2 % full scale	
	Thermocouple inputs	K: -200.0 to 1300.0°C or .300.0 to 2300.0°F J: -100.0 to 850.0°C or .100.0 to 1500.0°F	
Input signal range	Analog voltage inputs	0 to 10V/1 to 5V	
	Analog inputs	4 to 20mA	
Resolution	Thermocouple inputs	0.1°C or 0.1°F	
Resolution	Analog inputs	1/12000 (full scale)	
Max. rated input	Analog voltage inputs	±15V	
max. rated input	Analog inputs	±30mA	
External input	Analog voltage inputs	1MΩ min.	
impedance	Analog inputs	Approx. 250Ω	
Open-circuit detection	on function	Supported	
Averaging function		Unsupported	
Conversion time		250 ms for 4 input points	
Converted temperature data		16-bit binary data (4-digit hexadecimal)	
Converted AD data		16-bit binary data (4-digit hexadecimal)	
Isolation		Photocouplers between all temperature and analog input signals	
Current consumptio	n	5 VDC: 70 mA max., 24 VDC: 30 mA max.	

*1 Only last two channels can be used as analog input.
*2 Accuracy for a K-type sensor at -100°C or less is ±4°C ±1 digit max.
*3 Accuracy for a K-type sensor at -100°C or less is ±10°C ±1 digit max.

DIP Switch Settings

The DIP switch is used to set the input type (temperature or analog input), the input thermocouple type (K or J) and the temperature unit (°C or °F).

Note: Set the temperature range according to the type of temperature sensor connected to the Unit. Temperature data will not be converted correctly if the temperature range does not match the sensor.

SW		Setting		
	1	Thermocouple type of temperature	ON	J
	1	sensor	OFF	К
	2	Temperature unit	ON	°F
	2		OFF	°C
SW 1 2 3 4 5 6	3	NC		
	4	Input type selection for the third input (Input 2)	ON	Analog input
	4		OFF	Thermocouple
	5	Input type selection for the fourth	ON	Analog input
	5	input (Input 3)	OFF	Thermocouple
	6	Analog input signal range	ON	1 to 5V/4 to 20mA
	0		OFF	0 to 10V

Temperature input					
Input type	Range (°C)	Range (°F)			
к	-200.0 to 1300.0	-300 to 2300			
J	-100.0 to 850.0	-100.0 to 1500			

Main Specifications

Item		CP1W-TS004
T		Thermocouples
Temperature sensors	•	Switchable between K and J, but same type must be used for all inputs.
Number of inputs		12
25°C	(The larger of $\pm 0.5\%$ of converted value or $\pm 2^{\circ}$ C) ± 1 digit max. *1	
Accuracy	0 to 55°C	(The larger of $\pm 1\%$ of converted value or $\pm 4^{\circ}$ C) ± 1 digit max. *2
Conversion time		500 ms for 12 input points
Converted temperatu	re data	16-bit binary data (4-digit hexadecimal)
Isolation		Photocouplers between all temperature and analog input signals
Current consumption		5 VDC: 80 mA max., 24 VDC: 50 mA max.

*1 Accuracy for a K-type sensor at -100°C or less is $\pm 4^{\circ}C \pm 1$ digit max.

*2 Accuracy for a K-type sensor at -100°C or less is $\pm 10^\circ C \pm 1$ digit max.

DIP Switch Settings

The DIP switch is used to set the temperature unit and to set the temperature input range.

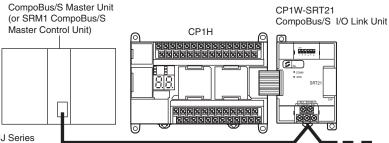
Note: Set the temperature range according to the type of temperature sensor connected to the Unit. Temperature data will not be converted correctly if the temperature range does not match the sensor.

SW			Setting	
SW 1 2	4	Input type	ON	J
	1	Input type	OFF	К
	2	Temperature unit	ON	°F
	2		OFF	Ο°

Temperature input					
Input type	Range (°C)	Range (°F)			
К	-200.0 to 1300.0	-300 to 2300			
J	-100.0 to 850.0	-100.0 to 1500			

CP1W-SRT21 CompoBus/S I/O Link Unit

The CompoBus/S I/O Link Unit functions as a slave for a CompoBus/S Master Unit (or an SRM1 CompoBus/S Master Control Unit) to form an I/O Link with 8 inputs and 8 outputs between the CompoBus/S I/O Link Unit and the Master Unit.



CS/CJ Series C200H□ Series CQM1(H) Series SRM1 Series CPM2C-S Series

Special flat cable or VCTF cable A maximum of 16 Units can be connected

Specifications

Item Model	CP1W-SRT21
Master/Slave	CompoBus/S Slave
Number of I/O bits	8 input bits, 8 output bits
Number of words occupied in CP1H I/O memory	1 input word, 1 output word (Allocated in the same way as for other Expansion Units)
Node number setting	Set using the DIP switch (before the CPU Unit is turned ON.)

I/O Bits and I/O Allocations

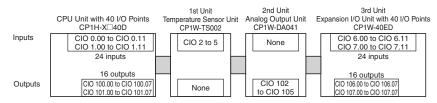
With CP1H CPU Units, the beginning input and output words (CIO 0 and CIO 100) are allocated by the CPU Unit one or two words at a time. I/O bits are allocated in word units in order of connection to Expansion Units and Expansion I/O Units connected to a CPU Unit.

CPU Unit	Allocated words		
	Inputs	Outputs	
CP1H CPU Unit with 40 I/O points	CIO 0 and CIO 1	CIO 100 and CIO 101	

Note: For details on the number of words allocated to Expansion Units and Expansion I/O Units, refer to Words Allocated to CP1W Expansion Units and Expansion I/O Units on page 26.

• Example: I/O Bit Allocations When Expansion Units Are Connected

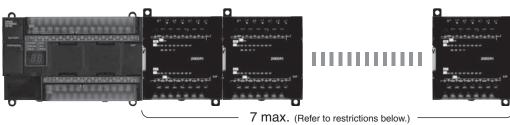
CPU Unit with 40 I/O Points + Temperature Sensor Unit + Analog Output Unit + Expansion I/O Unit with 40 I/O Points



Expansion Unit and Expansion I/O Units Specifications

■ Maximum Number of CP1W Expansion Unit and Expansion I/O Units

CP1H CPU Unit



■ Restrictions on the Number of CP1H Expansion Unit and I/O Unit Connections

Up to seven Expansion Units and Expansion I/O Units can be connected when a CP1H CPU Unit is used, but the following restrictions apply. Observe these restrictions when using the models in the shaded areas in the following tables. A maximum total of 15 input words is allocated for Expansion Units and a maximum total of 15 output words is allocated for Expansion Units and Expansion I/O Units.

 Words Allocated t 	o CP1W Expansion Un	its and Expansion I/O Units
---------------------------------------	---------------------	-----------------------------

Unit type		Mardal	No. of	words
	Unit type	Model	Input	Output
		CP1W-40EDR		
	40 I/O points	CP1W-40EDT	2	2
		CP1W-40EDT1		
		CP1W-32ER		
	32 outputs	CP1W-32ET		4
		CP1W-32ET1		
		CP1W-20EDR1		
Expansion	20 I/O points	CP1W-20EDT	1	1
I/O Units		CP1W-20EDT1		
		CP1W-16ER		
	16 outputs	CP1W-16ET		2
		CP1W-16ET1		
	8 inputs	CP1W-8ED	1	
	8 outputs	CP1W-8ER		
		CP1W-8ET		1
		CP1W-8ET1		
Analog Input	A/D: 4 points	CP1W-AD041	4	0
Unit	A/D. 4 points	CP1W-AD042	4	2
Angles Output	DA: 2 points	CP1W-DA021		2
Analog Output Unit	DA: 4 points	CP1W-DA041		4
	DA. 4 points	CP1W-DA042		4
	A/D: 2 points D/A: 1 point	CP1W-MAD11	2	1
Analog I/O Unit	A/D: 4 points D/A: 2 points	CP1W-MAD42	4	2
	A/D: 4 points D/A: 4 points	CP1W-MAD44	4	4
	Thermocouple (J or K)	CP1W-TS001	2	
		CP1W-TS002	4	
	Thermocouple (J or K)	CP1W-TS003	4	
Temperature Sensor Unit	Thermocouple (J or K) A/D: 2 points	CP1W-TS004	2	1
	Platinum resistance	CP1W-TS101	2	
	thermometer (Pt or JPt)	CP1W-TS102	4	
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CP1W-SRT21	1	1

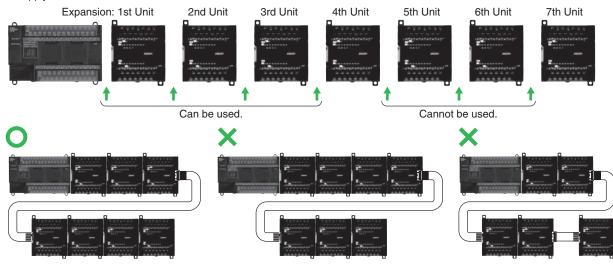
For example, the CP1W-TS002 Temperature Sensor Unit is allocated four words per Unit, so no more than three Units can be connected (4 words x 3 Units = 12 words). It would then be possible to mount a combination of other Units to use the remaining three input and 15 output words.

Examples of Possible Combinations

Number of Units	Input	Output
CP1H-X40DR-A		
CP1W-TS002 x 3	4 words x 3 Units = 12 words	0 words
CP1W -TS001 x 1	2 words x 1 Unit = 2 words	0 words
CP1W -20EDR1 x 1	1 word x 1 Unit = 1 word	1 word x 1 Unit = 1 word
CP1W - DA041 x 2	0 words	4 words x 2 Units = 8 words
Total: 7 Units	Total: 15 words	Total: 9 words
≤7 Units	≤15 words	≤15 words

■ Using CP1W-CN811 I/O Connecting Cable

- I/O Connecting Cable can be connected to any Unit from the CP1H CPU Unit to the third Expansion Unit or Expansion I/O Unit (i.e., the fourth Unit).
- Only one I/O Connecting Cable can be used in each CP1H PLC.
- Even when I/O Connecting Cable is used, the above restrictions on the number of connectable CP1W Expansion Units and Expansion I/O Units still apply.



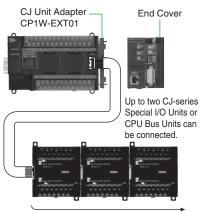
■ Using CJ-series Special I/O Units or CPU Bus Units with a CP1H CPU Unit

Up to two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CP1W-EXT01 CJ Unit Adapter. The number of Units that can be used is as described below.

.. . .

• CJ-series Special I/O Units and CPU Bus Units (For details, refer to the CJ1 Catalog (Cat. No. P052)).

5 V Current



7 max.

Use CP1W-CN811 I/O Connecting Cable when using CP1W Expansion Units and Expansion I/O Units at the same time as a CJ Unit Adapter. In this situation, the number of CP1W Expansion Unit and Expansion I/O Units that can be connected is subject to the restrictions described above. Only one I/O Connecting Cable can be used.

Unit name	Model	consumption (A)	
	CJ1W-AD042	0.52 A	
Analog Input Units	CJ1W-AD081-V1	0.42 A	
	CJ1W-AD041-V1	0.42 A	
	CJ1W-DA042V	0.40 A	
	CJ1W-DA08V	0.14 A	
Analog Output Units	CJ1W-DA08C	0.14 A	
	CJ1W-DA041	0.12 A	
	CJ1W-DA021	0.12 A	
Analog I/O Unit	CJ1W-MAD42	0.58 A	
	CJ1W-PH41U	0.30 A	
	CJ1W-AD04U	0.32 A	
Process Input Units	CJ1W-PTS51	0.25 A	
Frocess input onits	CJ1W-PTS52		
	CJ1W-PTS15		
	CJ1W-PDC15	0.10 A	
	CJ1W-TC001		
	CJ1W-TC002		
	CJ1W-TC003		
Temperature Control	CJ1W-TC004	0.25 A	
Units	CJ1W-TC101	0.25 A	
	CJ1W-TC102		
	CJ1W-TC103		
	CJ1W-TC104		
CompoBus/S Master Unit	CJ1W-SRM21	0.15 A	
CompoNet™ Master Unit	CJ1W-CRM21	0.40 A	

Unit name	Model	5 V Current consumption (A)
	CJ1W-NC113	0.05.4
	CJ1W-NC213	0.25 A
	CJ1W-NC413	0.36 A
Position Control Units	CJ1W-NC133	
	CJ1W-NC233	0.25 A
	CJ1W-NC433	0.36 A
High-speed Counter Unit	CJ1W-CT021	0.25 A
	CJ1W-V680C11	0.26 A (24 VDC 0.13 A)
ID Sensor Linite	CJ1W-V680C12	0.32 A (24 VDC 0.26 A)
ID Sensor Units	CJ1W-V600C11	0.26 A (24 VDC 0.12 A)
	CJ1W-V600C12	0.32 A (24 VDC 0.24 A)
	CJ1W-SCU42	0.38 A*
	CJ1W-SCU22	0.29 A*
Serial Communications	CJ1W-SCU32	0.46 A
Units	CJ1W-SCU41-V1	0.38 A*
	CJ1W-SCU21-V1	0.28 A*
	CJ1W-SCU31-V1	0.38 A
Ethernet Unit	CJ1W-ETN21	0.37 A
EtherNet/IP™ Unit	CJ1W-EIP21	0.41 A
DeviceNet [™] Unit	CJ1W-DRM21	0.33 A
Controller Link Unit	CJ1W-CLK23	0.35 A
	CJ1W-NC271	
MECHATROLINK-II	CJ1W-NC471	0.36 A
Position Control Unit	CJ1W-NCF71	0.30 A
	CJ1W-NCF71-MA	
MECHATROLINK-II Motion Control Unit	CJ1W-MCH71	0.6 A
FL-net Unit	CJ1W-FLN22	0.37 A
High-speed Data Storage Unit	CJ1W-SPU01-V2	0.56 A

 The current consumption increases by 0.15 A/Adapter er when NT-AL001 Link Adapters are used.

Current Consumption

 Based on the current consumption when CJ-series Special I/O Units or CPU Bus Units are used with a CP1H CPU Unit, the maximum number of Units that can be used is two CJ-series Units and seven CP1W Expansion Units and Expansion I/O Units.

The current consumption for the CP1H must be no more than 2 A for 5 V and 1 A for 24 V, and the total current consumption must be no more than 30 W.

Check the total current consumption to be sure these limits are not exceeded referring to page 28 for the CP1H CPU Unit and CP1W Expansion Unit and Expansion I/O Unit current consumptions and to the above table for CJ-series Unit current consumptions.

• CPU Units

Model	Current co	Current consumption	
Model	5 VDC	24 VDC	24 VDC (See note 5.)
CP1H-X40DR-A	0.42 A	0.07 A	0.3 A max. (0.9 A max.)
CP1H-X40DT-D	0.50 A	0.01 A	
CP1H-X40DT1-D	0.50 A	0.02 A	
CP1H-XA40DR-A	0.43 A	0.18 A	0.3 A max. (0.8 A max.)
CP1H-XA40DT-D	0.51 A	0.12 A	
CP1H-XA40DT1-D	0.51 A	0.15 A	
CP1H-Y20DT-D	0.55 A		

Note: 1. The current consumption of the CP1W-ME05M Memory Cassette and the CP1W-CIF01/CIF11 Option Boards are included in the current consumption of the CPU Unit.

2. CPU Units with DC power do not provide an external power supply.

3. The current consumptions given in the following table must be added to the current consumption of the CPU Unit if an Expansion Unit or Expansion I/O Unit is connected.

4. The external power supply cannot be used if an Expansion Unit or Expansion I/O Unit is connected to a CPU Unit with 14 or 20 I/O points.

5. Values in parentheses are the maximum external power supply for a CPU Unit to which an Expansion I/O Unit is not connected. Refer to the CP1H CPU Unit Operation Manual (Cat. No. W450) for details.

Option Units

Unit name	Model	Current co	Current consumption	
Unit name	Model	DC5V	24 VDC	
RS-232C Option Board	CP1W-CIF01	*		
RS-422A/485 Option Board	CP1W-CIF11	*		
RS-422A/485 (Isolated-type) Option Board	CP1W-CIF21	0.075A		
Ethernet Option Board	CP1W-CIF41	0.130A		
LCD Option Board	CP1W-DAM01	0.020A		
Memory Cassette	CP1W-ME05M	*		
CJ Unit Adapter	CP1W-EXT01	*		

* The current consumption of the following is included with the current consumption of the CPU Unit:

CP1W-ME05M Memory Cassette, CP1W-CIF-01 or CP1W-CIF11 Option Board, and CP1W-EXT01 CJ Unit Adapter.

• Others : Equipment that uses internal power supply of PLC

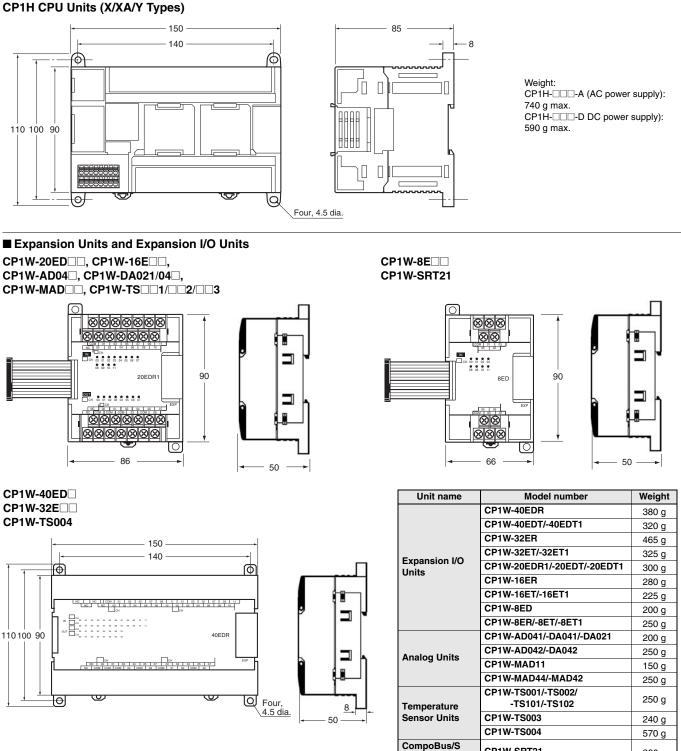
Unit name		Model	Current co	Current consumption	
		Model	5 VDC	24 VDC	
Link Adamtan		CJ1W-CIF11	0.04A		
Link Adapter		NT-AL001	0.15A		
Programmable Terminal NV3W-V1	Backlight (Green/Orange/Red)	NV3W-MG20L-V1	0.2A		
	Backlight (White/Pink/Red)	NV3W-MR20L-V1	0.2A		

• Expansion Units and Expansion I/O Units

Unit name		Madal	Current co	Current consumption		
		Model	5 VDC	24 VDC		
	40 I/O points	CP1W-40EDR	0.080 A	0.090 A		
	24 inputs	CP1W-40EDT				
	16 outputs	CP1W-40EDT1	0.160 A			
		CP1W-32ER	0.049 A	0.131 A		
	32 outputs	CP1W-32ET				
		CP1W-32ET1	0.113 A			
	20 I/O points	CP1W-20EDR1	0.103 A	0.044 A		
	12 inputs	CP1W-20EDT				
Expansion I/O Units	8 outputs	CP1W-20EDT1	0.130 A			
		CP1W-16ER	0.042 A	0.090 A		
	16 outputs	CP1W-16ET				
		CP1W-16ET1	0.076 A			
	8 inputs	CP1W-8ED	0.018 A			
	8 outputs	CP1W-8ER	0.026 A	0.044 A		
		CP1W-8ET				
		CP1W-8ET1	0.075 A			
	A/D: 4 points	CP1W-AD041	0.100 A	0.090 A		
Analog Input Unit		CP1W-AD042	0.100 A	0.050 A		
	DA: 2 points	CP1W-DA021	0.040 A	0.095 A		
Analog Output Unit	DA: 4 points	CP1W-DA041	0.080 A	0.124 A		
	DA: 4 points	CP1W-DA042	0.070 A	0.160 A		
	A/D: 2 points D/A: 1 point	CP1W-MAD11	0.083 A	0.110 A		
Analog I/O Unit	A/D: 4 points D/A: 2 points	CP1W-MAD42	0.120 A	0.120 A		
	A/D: 4 points D/A: 4 points	CP1W-MAD44	0.120 A	0.170 A		
	Thermocouple (J or K)	CP1W-TS001	0.040.4	0.050.4		
		CP1W-TS002	0.040 A	0.059 A		
Temperature Sensor Unit	Thermocouple (J or K)	CP1W-TS003	0.070 A	0.030 A		
remperature Sensor Unit	Thermocouple (J or K)	CP1W-TS004	0.080 A	0.050 A		
	Platinum resistance	CP1W-TS101	0.054.4	0.070.4		
	thermometer (Pt or JPt)	CP1W-TS102	0.054 A	0.073 A		
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CP1W-SRT21	0.029 A			

Dimensions

■ CPU Units



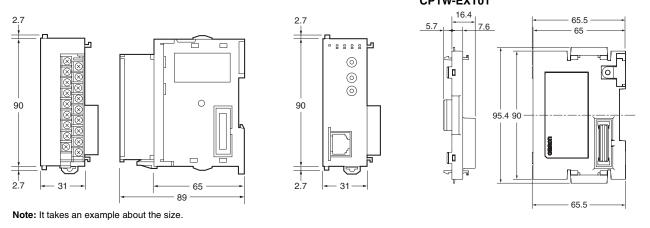
CP1W-SRT21

I/O Link Unit

200 g

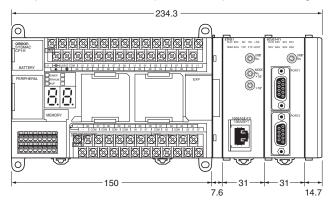
■ CJ-series Special I/O Units and CPU Bus Units





■CP1H

Example: Two CJ-series Units (31-mm widths) Connected Using a CJ Unit Adapter



CP1H

Related Manuals

Cat. No.	Model numbers	Manual name	Description
W450	CP1H-X40DD-D CP1H-XA40DD-D CP1H-Y20DT-D	CP Series CP1H CPU Unit Operation Manual	Provides the following information on the CP Series: • Overview, design, installation, maintenance, and other basic specifications • Features • System configuration • Mounting and wiring • I/O memory allocation • Troubleshooting Use this manual together with the <i>CP1H Programmable Controllers Programming</i> <i>Manual</i> (W451).
W451	CP1H-X40DD-D CP1H-XA40DD-D CP1H-Y20DT-D	CP Series CP1H CPU Unit Programming Manual	Provides the following information on the CP Series: • Programming instructions • Programming methods • Tasks • File memory • Functions Use this manual together with the <i>CP1H Programmable Controllers Operation</i> <i>Manual</i> (W450).
W342	CS1G/H-CPU H CS1G/H-CPU H CS1D-CPU H CS1D-CPU S CS1W-SCU21 CS1W-SCB21-V1/41-V1 CJ1G/H-CPU H CJ1G-CPU P CP1H-CPU C CJ1G-CPU C CJ1G-CPU C CJ1G-CPU C	CS/CJseries Communications Commands Reference Manual	 Describes commands addressed to CS-series and CJ-series CPU Units, including C-mode commands and FINS commands. Note: This manual describes on commands address to CPU Units regardless of the communications path. (CPU Unit serial ports, Serial Communications Unit/Board ports, and Communications Unit ports can be used.) Refer to the relevant operation manuals for information on commands addresses to Special I/O Units and CPU Bus Units.

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